

## DEVICE AND METHOD FOR PROTECTING GATE TERMINAL AND LEAD

### ABSTRACT OF THE DISCLOSURE

A resist region covering the gate terminal and lead of the gate electrode line and between a passivation layer and a gate insulating layer is used to protect the gate terminal and lead. The resist region is located at a scribing line on margin of the color filter substrate of a panel, thereby the resist region can protect the passivation layer and the gate insulating layer from cracking, and the gate terminal and the lead from corrosion after a portion of the color filter substrate is removed along the scribing line.